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Li-Minn Ang; Hon Nin Cheung;
[Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposium on](#)
Volume 4, 6-9 May 2001 Page(s):518 - 521 vol. 4
Digital Object Identifier 10.1109/ISCAS.2001.922288
[AbstractPlus](#) | Full Text: [PDF](#)(348 KB) IEEE CNF
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- ☐ 2. **An interprocessor memory access arbitrating scheme for the S-3800 vector supercomputer**
Sakakibara, T.; Kitai, K.; Isobe, T.; Yazawa, S.; Tanaka, T.; Tamaki, Y.; Inagami, Y.;
[Parallel Architectures, Algorithms and Networks, 1994. \(ISPAN\) International Symposium on](#)
14-16 Dec. 1994 Page(s):262 - 269
Digital Object Identifier 10.1109/ISPAN.1994.367140
[AbstractPlus](#) | Full Text: [PDF](#)(456 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **Accounting for memory bank contention and delay in high-bandwidth multiprocessors**
Belloc, G.E.; Gibbons, P.B.; Matias, Y.; Zagha, M.;
[Parallel and Distributed Systems, IEEE Transactions on](#)
Volume 8, Issue 9, Sept. 1997 Page(s):943 - 958
Digital Object Identifier 10.1109/71.615440
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(296 KB) IEEE JNL
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- ☐ 4. **Address assignment sensitive variable partitioning and scheduling for DSPS with multiple memory banks**
Chun Jason Xue; Tiantian Liu; Zili Shao; Jingdong Hu; Zhiping Jia; Weijia Jia; Sha, E.H.-M.;
[Acoustics, Speech and Signal Processing, 2008. ICASSP 2008. IEEE International Conference on](#)
March 31 2008-April 4 2008 Page(s):1453 - 1456
Digital Object Identifier 10.1109/ICASSP.2008.4517894
[AbstractPlus](#) | Full Text: [PDF](#)(236 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 5. **Dual Run-time Environments for Dual Data Memory Bank Architecture**
Cho, Jeonghun; Kwon, Soohyun; Park, Jinwhi; Kim, Jungheung;
[TENCON 2006. 2006 IEEE Region 10 Conference](#)
14-17 Nov. 2006 Page(s):1 - 4
Digital Object Identifier 10.1109/TENCON.2006.344087
[AbstractPlus](#) | Full Text: [PDF](#)(3762 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **6. Reducing memory bank conflict for embedded multimedia systems**
 Qi Zhang; Qing Li; Yunyang Dai; Kuo, C.-C.J.;
[Multimedia and Expo, 2004. ICME '04, 2004 IEEE International Conference on](#)
 Volume 1, 30-30 June 2004 Page(s):471 - 474 Vol.1
 Digital Object Identifier 10.1109/ICME.2004.1394231

[AbstractPlus](#) | Full Text: [PDF](#)(285 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **7. Memory Bank Aware Dynamic Loop Scheduling**
 Kandemir, M.; Yemliha, T.; Seung Woo Son; Ozturk, O.;
[Design, Automation & Test in Europe Conference & Exhibition, 2007. DATE '07](#)
 16-20 April 2007 Page(s):1 - 6
 Digital Object Identifier 10.1109/DATE.2007.364542

[AbstractPlus](#) | Full Text: [PDF](#)(320 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **8. Embedded memory in system-on-chip design: architecture and prototype implementation**
 Huang Jin; Manjikian, N.;
[Electrical and Computer Engineering, 2003. IEEE CCECE 2003. Canadian Conference on](#)
 Volume 1, 4-7 May 2003 Page(s):141 - 146 vol.1

[AbstractPlus](#) | Full Text: [PDF](#)(494 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **9. Variable partitioning for dual memory bank DSPs**
 Leupers, R.; Kotte, D.;
[Acoustics, Speech, and Signal Processing, 2001. Proceedings. \(ICASSP '01\), 2001 IEEE](#)
[International Conference on](#)
 Volume 2, 7-11 May 2001 Page(s):1121 - 1124 vol.2
 Digital Object Identifier 10.1109/ICASSP.2001.941118

[AbstractPlus](#) | Full Text: [PDF](#)(484 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **10. An Approach for Automatic Data Allocation in C to HDL Compilers**
 Maruyama, T.;
[Field-Programmable Custom Computing Machines, 2001. FCCM '01. The 9th Annual IEEE](#)
[Symposium on](#)
 2001 Page(s):255 - 256

[AbstractPlus](#) | Full Text: [PDF](#)(128 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **11. Retargetable compilation for low power**
 Wen-Tsong Shiue;
[Hardware/Software Codesign, 2001. CODES 2001. Proceedings of the Ninth International](#)
[Symposium on](#)
 25-27 April 2001 Page(s):254 - 259
 Digital Object Identifier 10.1109/HSC.2001.924685

[AbstractPlus](#) | Full Text: [PDF](#)(484 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **12. Memory bank customization and assignment in behavioral synthesis**
Panda, P.R.;
Computer-Aided Design, 1999. Digest of Technical Papers. 1999 IEEE/ACM International Conference on
7-11 Nov. 1999 Page(s):477 - 481
Digital Object Identifier 10.1109/ICCAD.1999.810697

[AbstractPlus](#) | Full Text: [PDF](#)(436 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **13. Memory bank and register allocation in software synthesis for ASIPs**
Sudarsanam, A.; Malik, S.;
Computer-Aided Design, 1995. ICCAD-95. Digest of Technical Papers., 1995 IEEE/ACM International Conference on
5-9 Nov. 1995 Page(s):388 - 392
Digital Object Identifier 10.1109/ICCAD.1995.480145

[AbstractPlus](#) | Full Text: [PDF](#)(548 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **14. Nonprime memory systems and error correction in address translation**
Katti, R.S.;
Computers, IEEE Transactions on
Volume 46, Issue 1, Jan. 1997 Page(s):75 - 79
Digital Object Identifier 10.1109/12.559804

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(120 KB) IEEE JNL
[Rights and Permissions](#)

- ☐ **15. Multi-port Memory Design Methodology Based on Block Read and Write**
Weixing Ji; Feng Shi; Baojun Qiao; Hong Song;
Control and Automation, 2007. ICCA 2007. IEEE International Conference on
May 30 2007-June 1 2007 Page(s):256 - 259
Digital Object Identifier 10.1109/ICCA.2007.4376358

[AbstractPlus](#) | Full Text: [PDF](#)(3520 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **16. A Pipeline Implementation of a Watershed Algorithm on FPGA**
Dang Ba Khac Trieu; Maruyama, T.;
Field Programmable Logic and Applications, 2007. FPL 2007. International Conference on
27-29 Aug. 2007 Page(s):714 - 717
Digital Object Identifier 10.1109/FPL.2007.4380752

[AbstractPlus](#) | Full Text: [PDF](#)(387 KB) IEEE CNF
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- ☐ **17. Energy-efficient backend compiler design for embedded systems**
Wen-Tsong Shine;
Electrical and Electronic Technology, 2001. TENCON. Proceedings of IEEE Region 10 International Conference on
Volume 1, 19-22 Aug. 2001 Page(s):103 - 109 vol.1
Digital Object Identifier 10.1109/TENCON.2001.949560

[AbstractPlus](#) | Full Text: [PDF](#)(536 KB) IEEE CNF
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- ☐ **18. Hierarchical memory mapping during synthesis in FPGA-based reconfigurable computers**
 Ouass, I.; Vemuri, R.;
Design, Automation and Test in Europe, 2001. Conference and Exhibition 2001. Proceedings
 13-16 March 2001 Page(s):650 - 657
 Digital Object Identifier 10.1109/DATe.2001.915092
[AbstractPlus](#) | Full Text: [PDF](#)(572 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **19. Memory bank conflict on some vector supercomputers**
 Fujino, S.;
CompEuro '92. 'Computer Systems and Software Engineering' Proceedings.
 4-8 May 1992 Page(s):359 - 364
 Digital Object Identifier 10.1109/CMPEUR.1992.218407
[AbstractPlus](#) | Full Text: [PDF](#)(396 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **20. Multi-port Memory Design Methodology Based on Block Read and Write**
 Weixing Ji; Feng Shi; Baojun Qiao; Hong Song;
Control and Automation, 2007. ICCA 2007. IEEE International Conference on
 May 30 2007-June 1 2007 Page(s):256 - 259
[AbstractPlus](#) | Full Text: [PDF](#)(3520 KB) IET CNF
- ☐ **21. Vector Computer Memory Bank Contention**
 Bailey, D.H.;
Computers, IEEE Transactions on
 Volume C-36, Issue 3, March 1987 Page(s):293 - 298
 Digital Object Identifier 10.1109/TC.1987.1676901
[AbstractPlus](#) | Full Text: [PDF](#)(2087 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ **22. Access pattern restructuring for memory energy**
 De La Luz, V.; Kadayif, I.; Kandemir, M.; Sezer, U.;
Parallel and Distributed Systems, IEEE Transactions on
 Volume 15, Issue 4, April 2004 Page(s):289 - 303
 Digital Object Identifier 10.1109/TPDS.2004.1271179
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(1208 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ **23. Power Consumption Reduction in Java-enabled, Battery-powered Handheld Devices through Memory Compression**
 Kato, M.; Lo, C.-T.D.;
Consumer Electronics, 2007. ISCE 2007. IEEE International Symposium on
 20-23 June 2007 Page(s):1 - 6
 Digital Object Identifier 10.1109/ISCE.2007.4382127
[AbstractPlus](#) | Full Text: [PDF](#)(178 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **24. An EPC Gen 2 compatible passive/semi-active UHF RFID transponder with embedded FeRAM and temperature sensor**
Shiho Kim; Jung-Hyun Cho; Hyun-Sik Kim; Haksoo Kim; Hee-Bok Kang; Suk-Kyung Hong;
[Solid-State Circuits Conference, 2007. ASSCC '07. IEEE Asian](#)
12-14 Nov. 2007 Page(s):135 - 138
Digital Object Identifier 10.1109/ASSCC.2007.4425750
[AbstractPlus](#) | [Full Text: PDF\(1592 KB\)](#) | [IEEE CNF](#)
[Rights and Permissions](#)

- ☐ **25. Cache Miss Clustering for Banked Memory Systems**
Ozturk, O.; Chen, G.; Kandemir, M.; Karakoy, M.;
[Computer-Aided Design, 2006. ICCAD '06. IEEE/ACM International Conference on](#)
Nov. 2006 Page(s):244 - 250
Digital Object Identifier 10.1109/ICCAD.2006.320143
[AbstractPlus](#) | [Full Text: PDF\(9769 KB\)](#) | [IEEE CNF](#)
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